

1 5. (Amended) AGC circuit according to claim 3, characterized in
2 that the gain variation range of the continuously controlled
3 amplifier caused defined by the range of the continuous gain
4 control signal between the negative and positive threshold levels,
5 corresponds at least to the gain variation of the digitally
6 controlled amplifier over two consecutive incremental steps of said
7 digital gain control signal.

1 6. (Amended) AGC circuit according to claim 4, characterized in
2 that the time period between two consecutive clock pulses of the
3 clock-signal is chosen sufficiently large to prevent superposition
4 of subsequent gain step variations of the digitally controlled
5 amplifier from occurring.

1 7. (Amended) AGC circuit according to claim 1, characterized in
2 that the time-constant of the loop-filter is chosen sufficiently
3 large to prevent regenerative feedback of the gain control signal
4 in the AGC loop from occurring.

1 8. (Amended) Receiver for digitally modulated signals comprising
2 an AGC circuit as claimed in claim 1, characterized by said
3 digitally controlled amplifier being coupled between an RF input
4 filter and a phase quadrature mixer stage, phase quadrature outputs
5 thereof being coupled through frequency selective means to a pair
6 of phase quadrature continuously controlled amplifiers, this pair
7 of phase quadrature continuously controlled amplifiers being
8 coupled through to a pair of phase quadrature analogue to digital
9 converters to said level detector.